



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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RS

8-27-01

Applicant(s): Andrew M. Welin

Art Unit: Not Assigned

Serial No.: 09/785,768

Examiner: Not Assigned

Filed: 02/16/01

Docket: TI-29045

For: Systems, Processes and Integrated Circuits for Improved Packet Scheduling of Media Over Packet

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents Application Processing Division's Customer Correction Branch, Washington, D.C. 20231.

Karen Vertz
Karen Vertz

3-14-01
Date

Assistant Commissioner for
Patents
Washington, D. C. 20231

PRELIMINARY AMENDMENT

Dear Sir:

Prior to the examination of the above identified application, please amend as follows:

IN THE CLAIMS:

Add the following new claims:

131. An electronic system comprising:

a processor circuit;

an egress packet control having stored instructions that generate for first and second received packets respective deadline intervals and order the processing in the processor circuit of the first and second received packets according to the respective deadline intervals; and

a printed wiring assembly bearing the processor circuit and the egress packet control.